



Multiprocessor Systems-on-Chips (Systems on Silicon)

Ahmed Jerraya, Wayne Wolf

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Multiprocessor Systems-on-Chips (Systems on Silicon) Ahmed Jerraya, Wayne Wolf Modern system-on-chip (SoC) design shows a clear trend toward integration of multiple processor cores on a single chip. Designing a multiprocessor system-on-chip (MPSOC) requires an understanding of the various design styles and techniques used in the multiprocessor. Understanding the application area of the MPSOC is also critical to making proper tradeoffs and design decisions.

Multiprocessor Systems-on-Chips covers both design techniques and applications for MPSOCs. Design topics include multiprocessor architectures, processors, operating systems, compilers, methodologies, and synthesis algorithms, and application areas covered include telecommunications and multimedia. The majority of the chapters were collected from presentations made at the International Workshop on Application-Specific Multi-Processor SoC held over the past two years. The workshop assembled internationally recognized speakers on the range of topics relevant to MPSOCs. After having refined their material at the workshop, the speakers are now writing chapters and the editors are fashioning them into a unified book by making connections between chapters and developing common terminology.

- *Examines several different architectures and the constraints imposed on them
- *Discusses scheduling, real-time operating systems, and compilers
- *Analyzes design trade-off and decisions in telecommunications and multimedia applications



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